

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 49657-217		SERIAL NO.	
				APPLICANT Shigeki OHBAYASHI			
				FILING DATE November 20, 1998		GROUP 2133	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
E	5,703,510	Dec. 30, 1997	Iketani et al.	327	143		
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
E	06-069425	March 11, 1994	Japan (with English abstract)	H01L	27/04		
E	06-005069	Jan. 14, 1994	Japan (with English abstract)	G11C	11/401		
E	02-302987	Dec. 14, 1990	Japan (with English abstract)	G11C	11/401		
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
E	JEDEC Standard No. 21-C, JEDEC, December 22, 1994, pp. 3.7.8-7						
E	"BGA Packages, Gaining Popularity in U.S.; Make High-Density Mounting of High Pin Count LSIs Easy", A. Fukuda, Nikkei Electronics, Feb. 14, 1994, pp. 59-73.						
E	M5M5V1132FP, GP-6, -7, -8, -7L, -8L, Mitsubishi's Product Data Sheet March 24, 1997, pp. 1-14.						
EXAMINER <i>Guy Ramare</i>				DATE CONSIDERED <i>11/12/01</i>			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.